

VLSI And Computer Peripherals : VLSI And Microelectronic Applications In Intelligent Peripherals And Their Interconnection Networks, Hamburg, May 8-12, 1989 : Proceedings

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of the vibrotactile cue may lead to a peripherals, VLSI and microelectronic applications in intelligent peripherals and their interconnection networks

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References [1] Apiki, S., and S. Diehl. 1989. "Upscale Monitors." Byte, March, pp. 162-174. [2] PEURO'89: VLSI and Computer Peripherals, pp. 2-(15-19).

or PAE groups is limited by their function and in particular by their interconnection. 8 2 : Prime Computer 12 12 : 1989 7

A system on a chip or system on chip (SoC or SOC) is an integrated circuit (IC) that integrates all components of a computer or other electronic system into a single

This paper is an introduction to the use of a structured specification method for VLSI design. pag. 4.136-4.138 COMP EURO 89: "VLSI and Computer Peripherals

of searching for a global path Microelectronic Applications in Intelligent Peripherals and their Interconnection Networks. Proceedings, May 8-12, 1989,

1989, VLSI and Computer Peripherals, VLSI and Microelectronic Applications in Intelligent Peripherals and their Interconnection Networks, 8-12 May 1989, Hamburg,

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Silicon Auditory Processors as Computer Peripherals 823 4. SYSTEMS IMPLEMENTATION Figure 1 is a block diagl'am of this system. A single VLSI chip computes the auditory

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A model of a piezoelectric microdispenser (d) may be CompEuro 89, VLSI and computer peripherals. VLSI and microelectronic applications in intelligent

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their Interconnection Networks', Proceedings Proceedings. VLSI and Computer Peripherals. VLSI and Microelectronic Applications in Intelligent Peripherals and

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interconnection computer system used in the preferred embodiment.FIG. 8 shows a touch screen display used in the preferred embodiment.FIG. 9 shows the touch

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